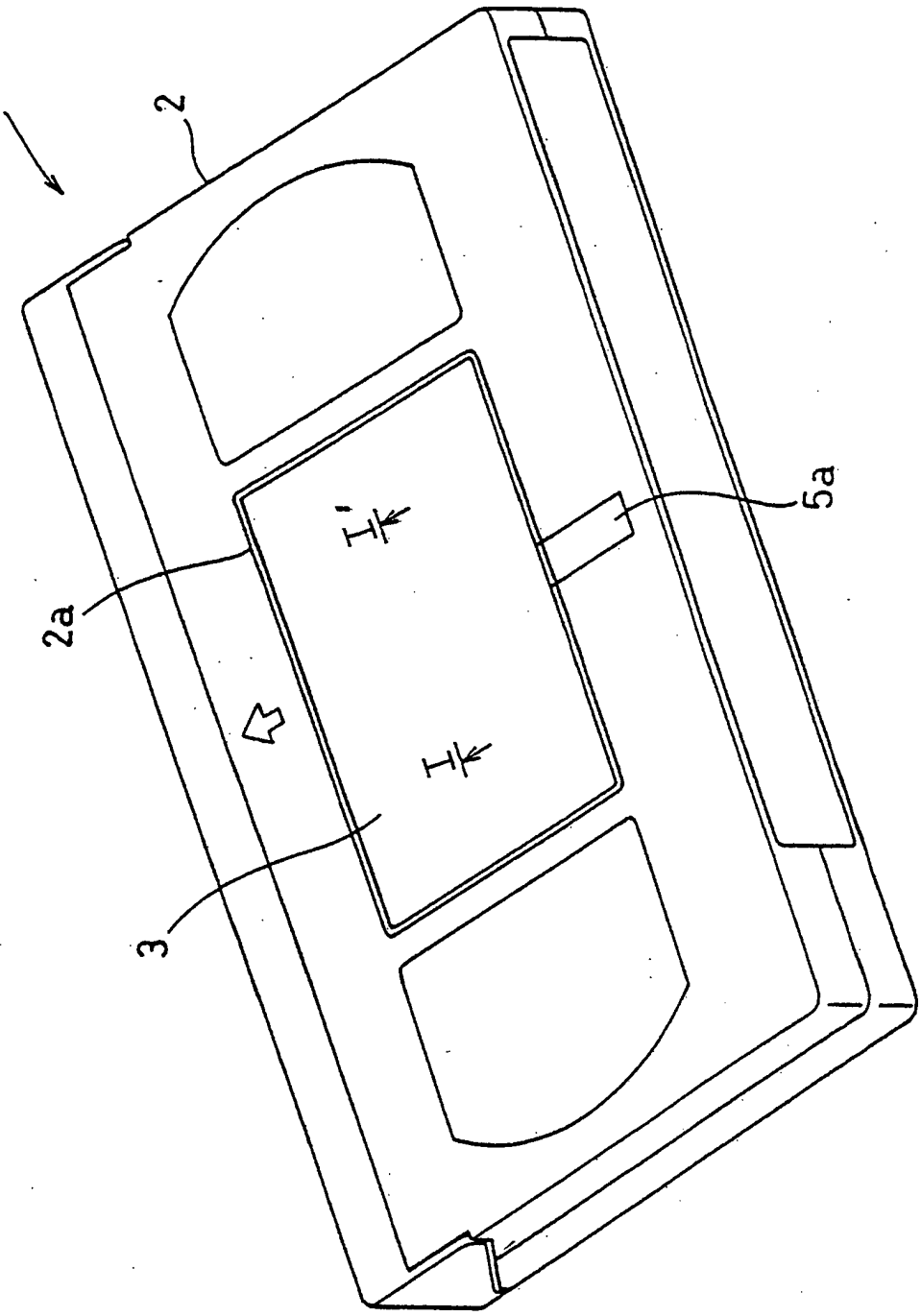

FIG.1

FIG. 2

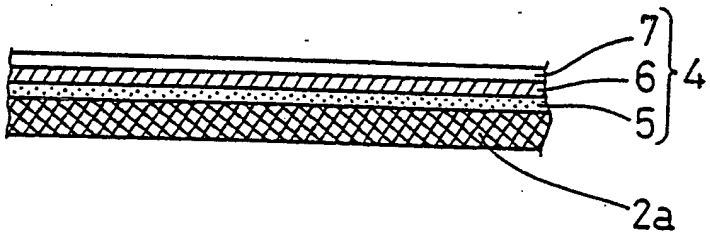

FIG. 3

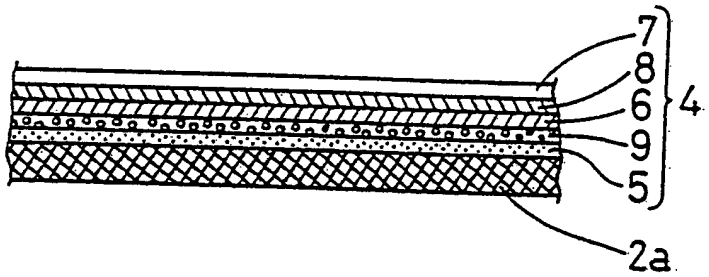

FIG.4

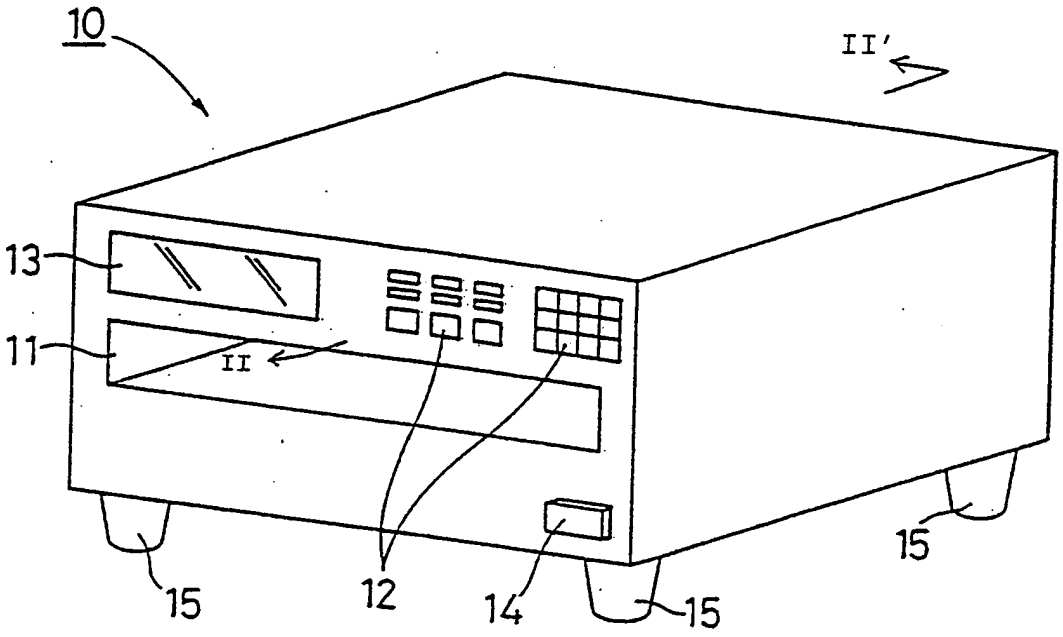

FIG.5

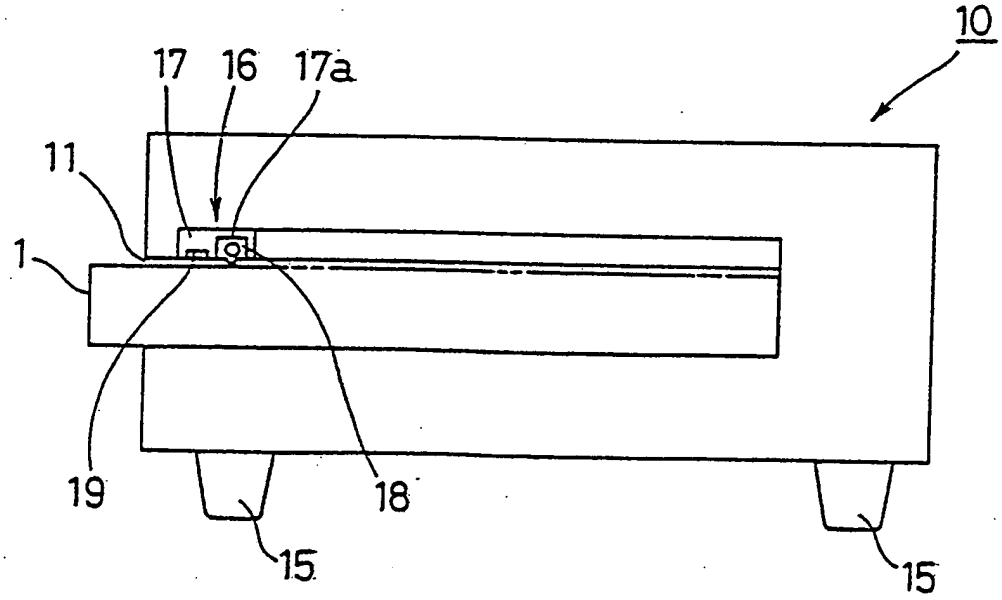

FIG. 6

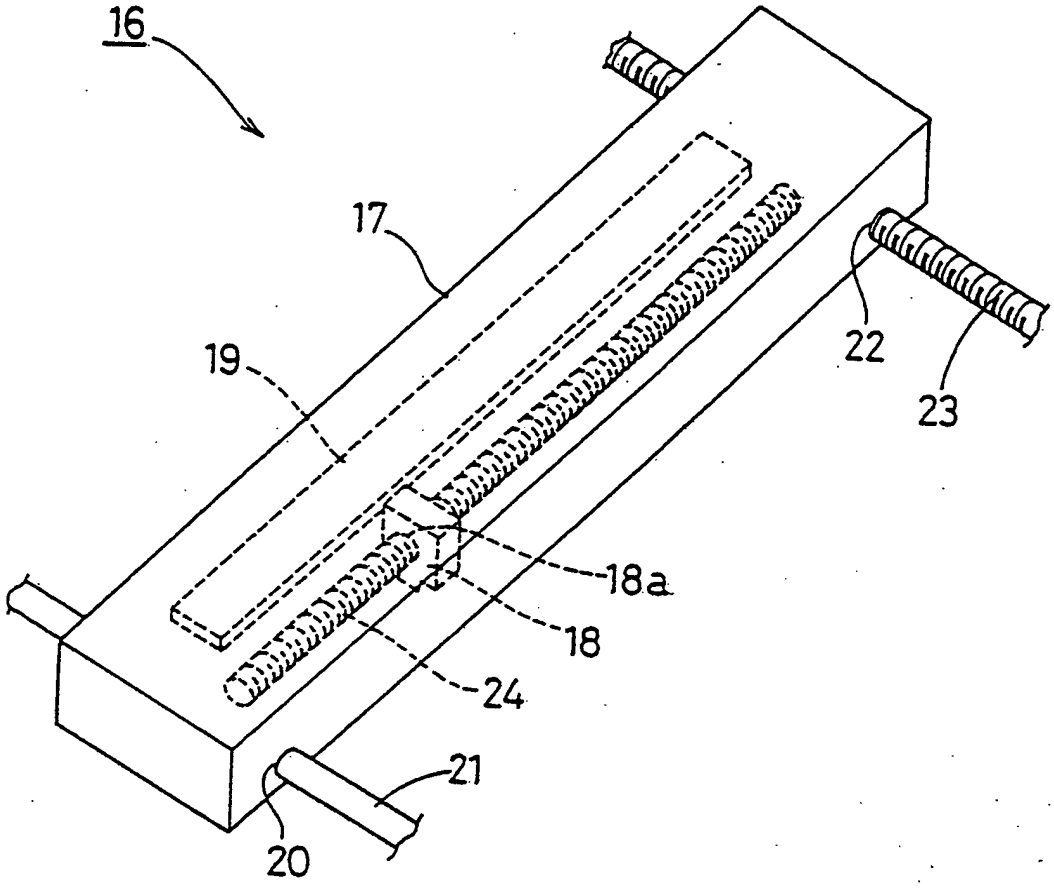

FIG. 7

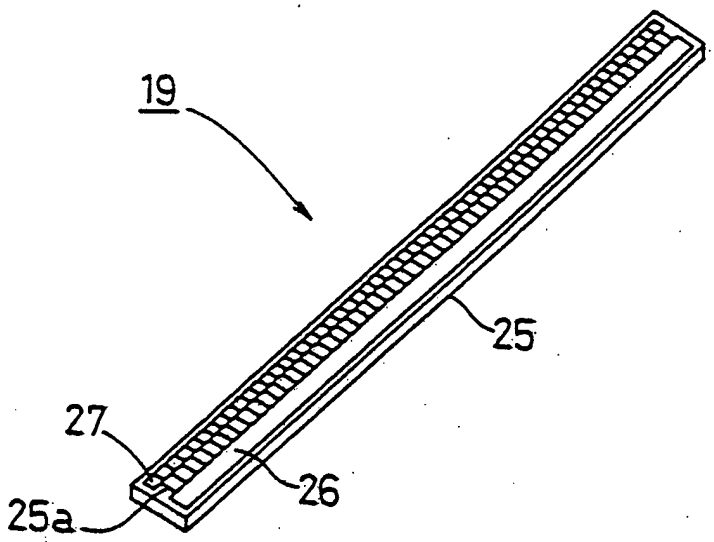

FIG. 8

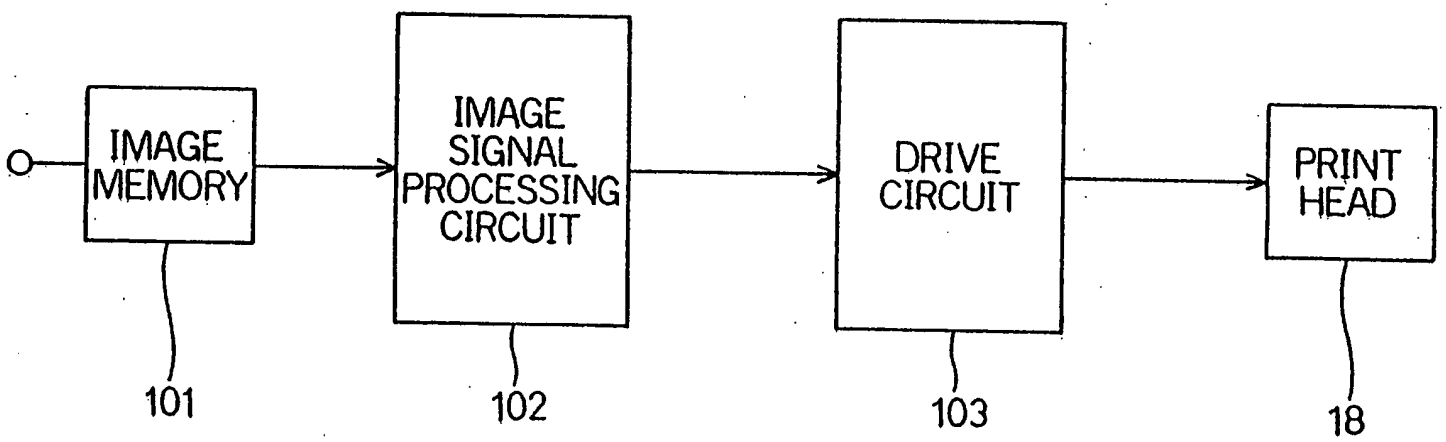
IMAGE MEMORY
IMAGE SIGNAL PROCESSING CIRCUIT
DRIVE CIRCUIT
PRINT HEAD
101
102
103
18

FIG.9

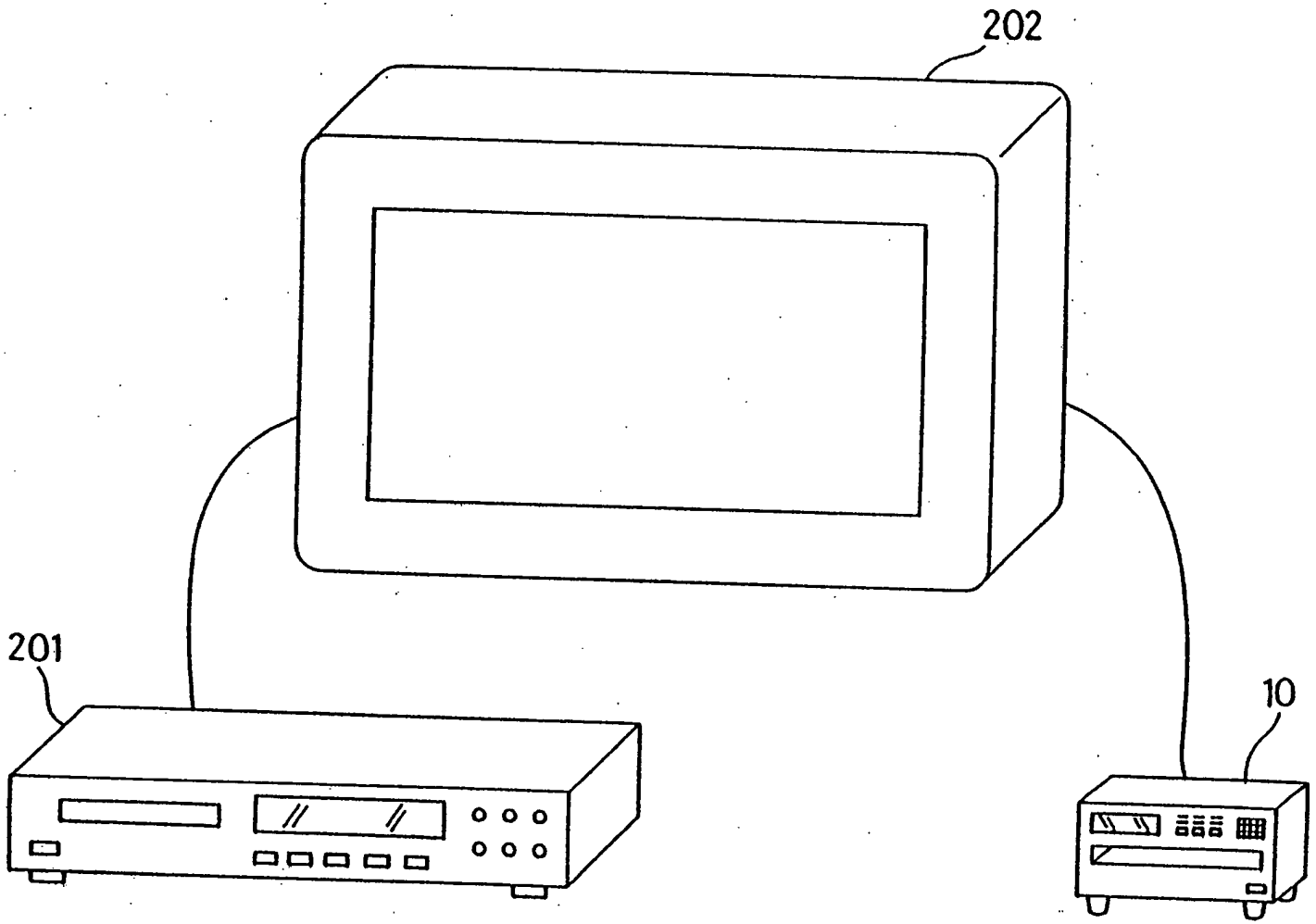

FIG.10

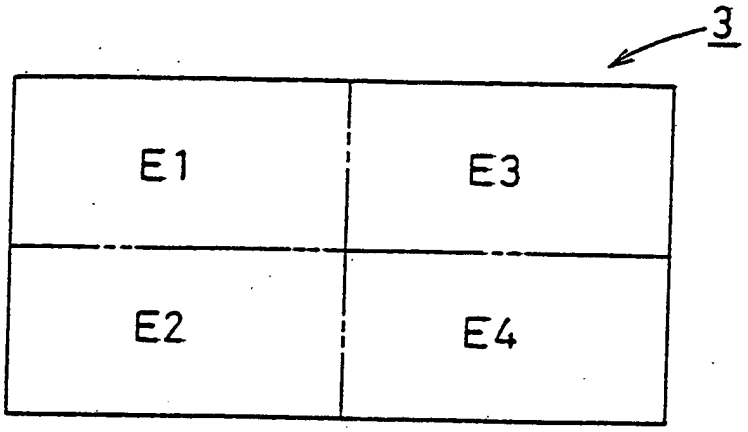

FIG.11

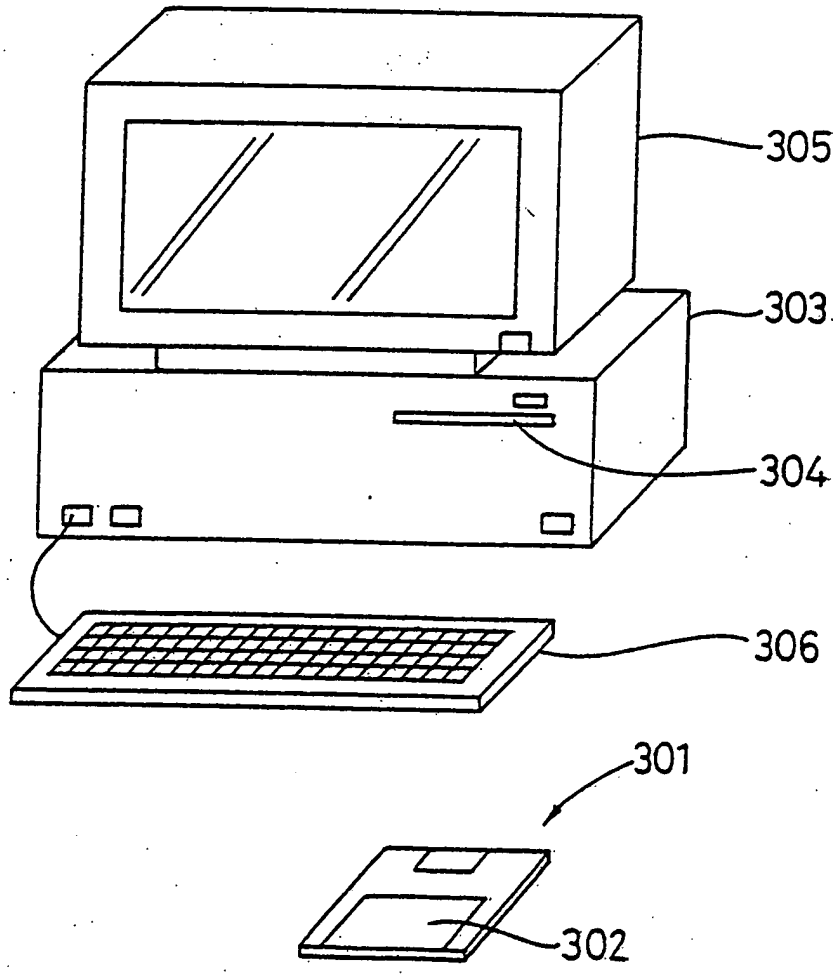

FIG. 12

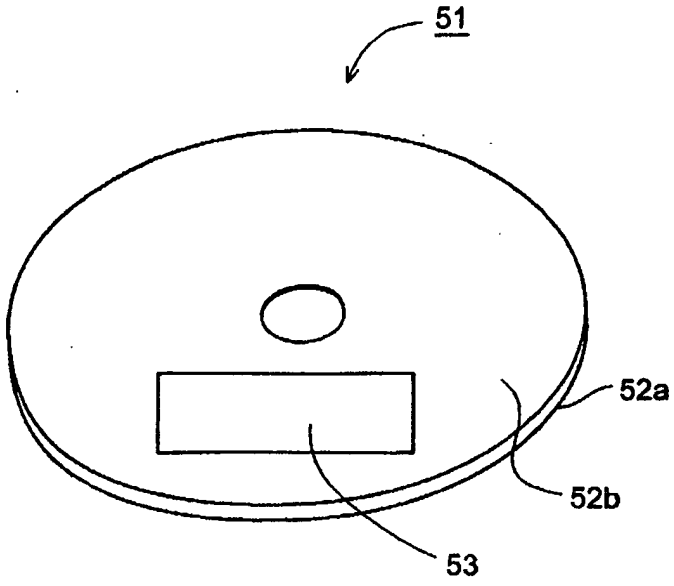

FIG. 13

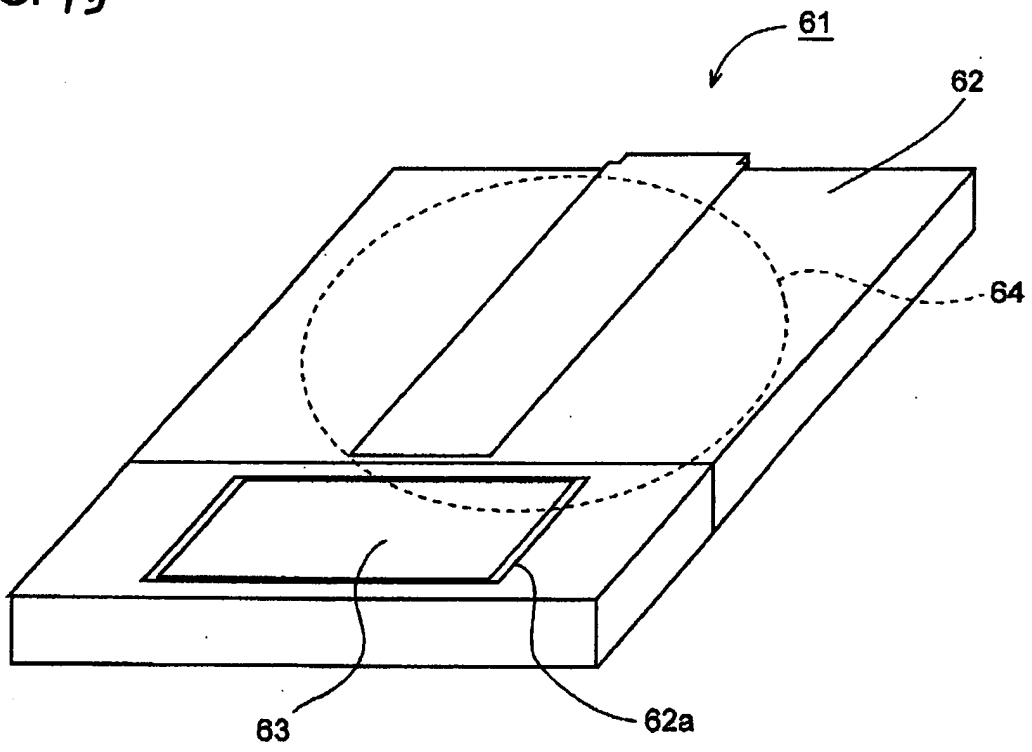

FIG. 14

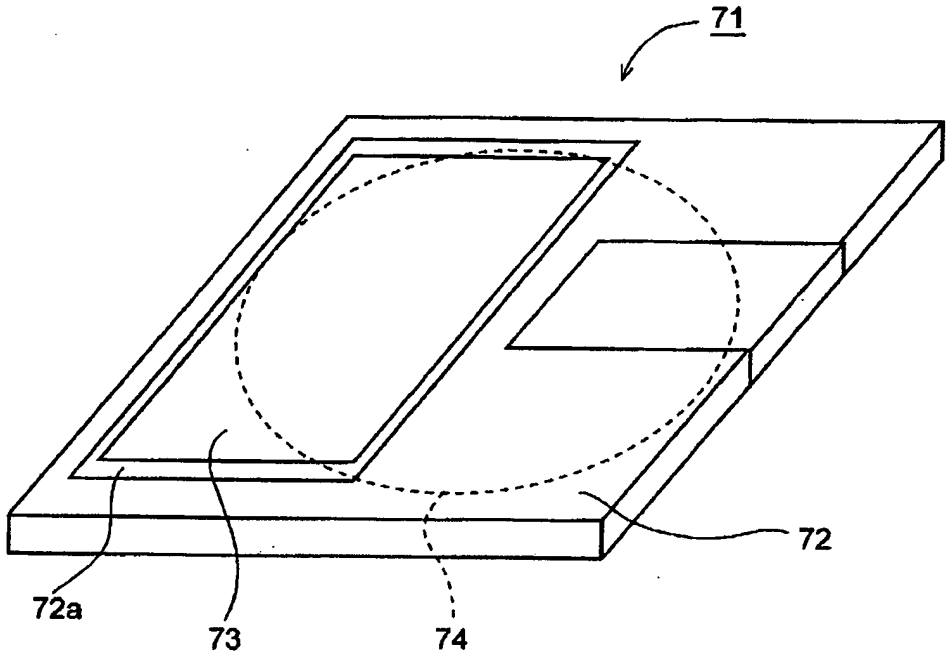

FIG. 15

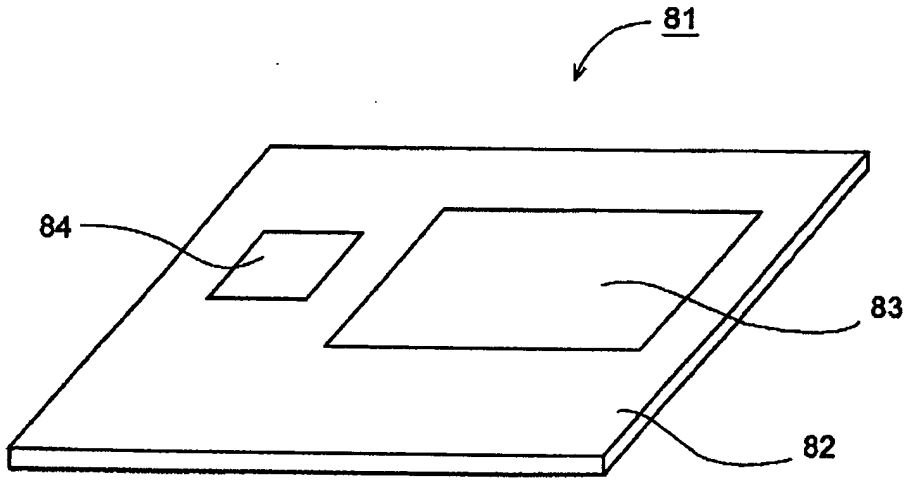

FIG. 16

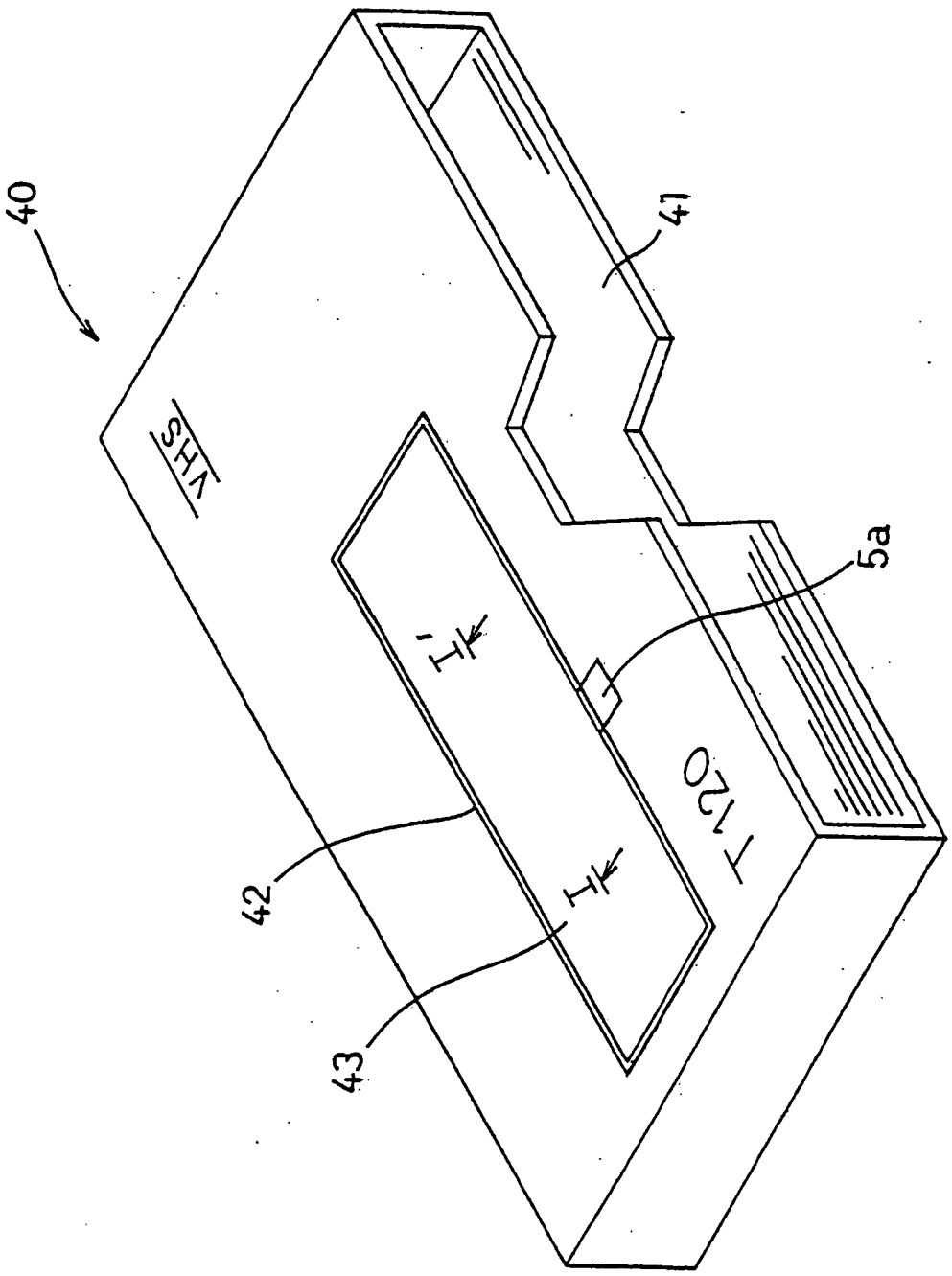

FIG. 17

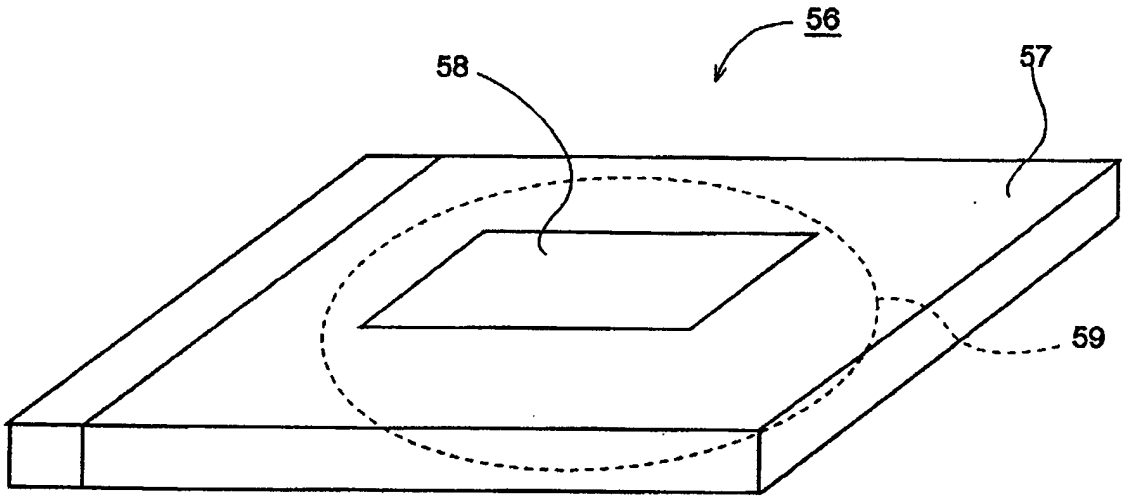

FIG. 18

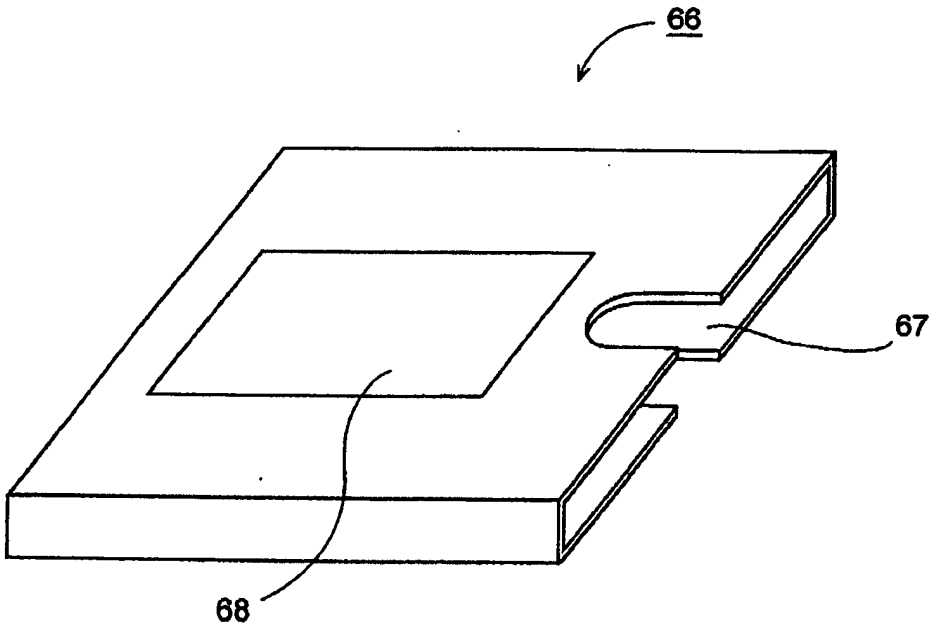

FIG. 19

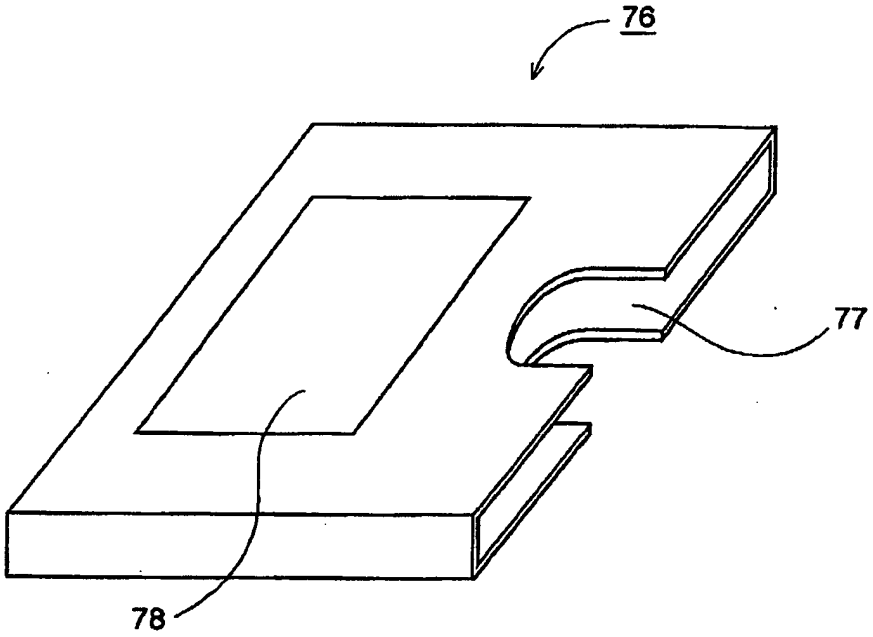

FIG. 20A

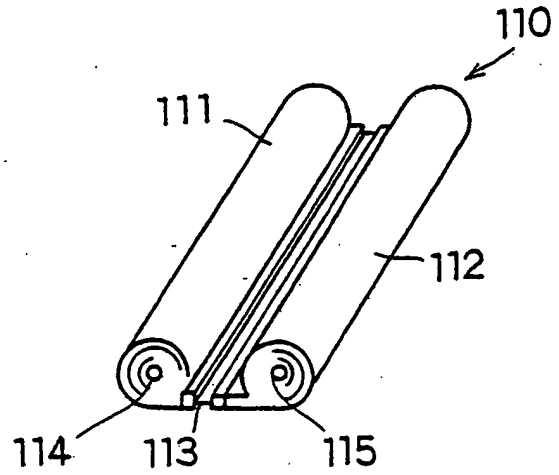

FIG. 20B

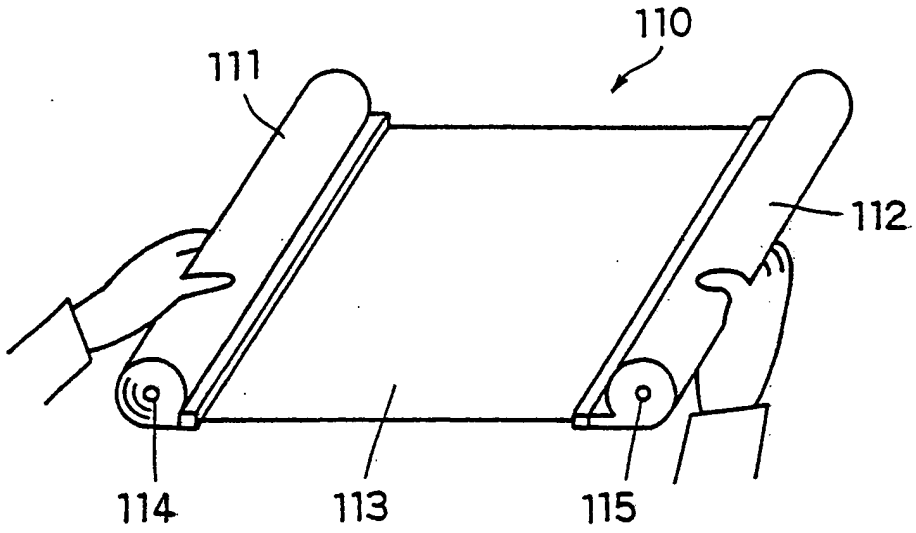

FIG. 21A

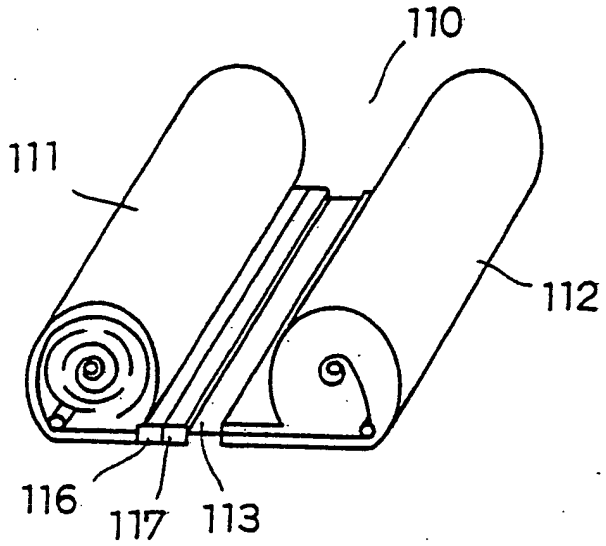

FIG. 21B

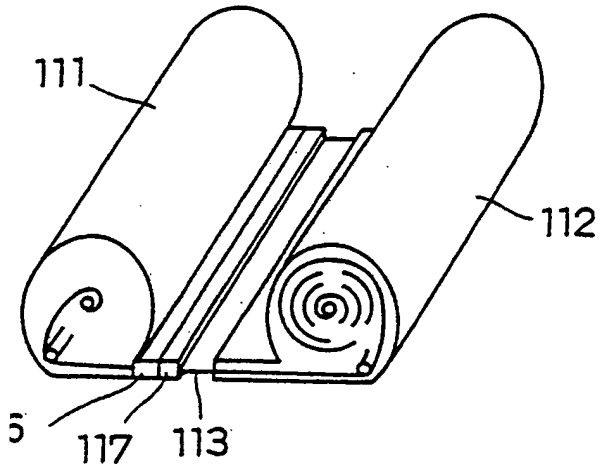

FIG. 21C

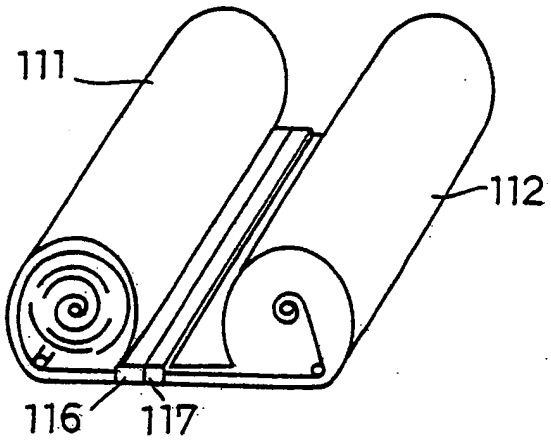

FIG. 22

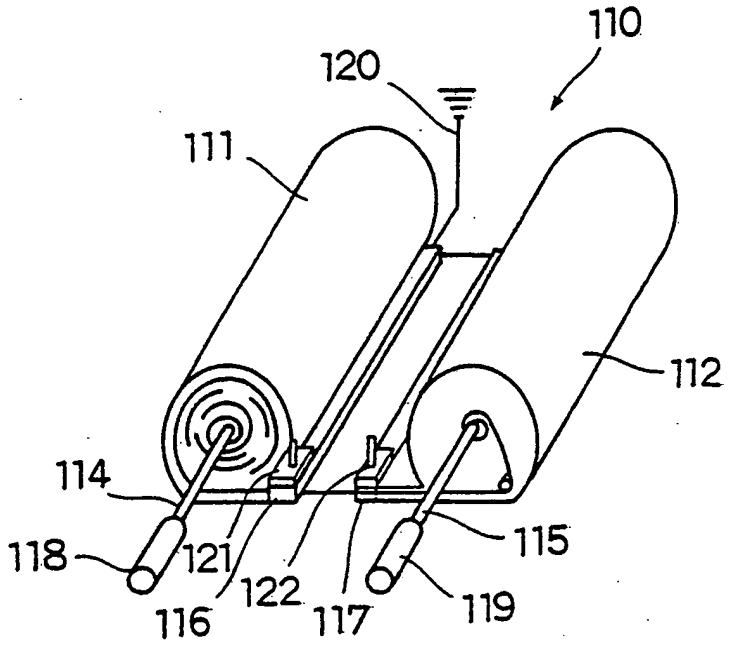
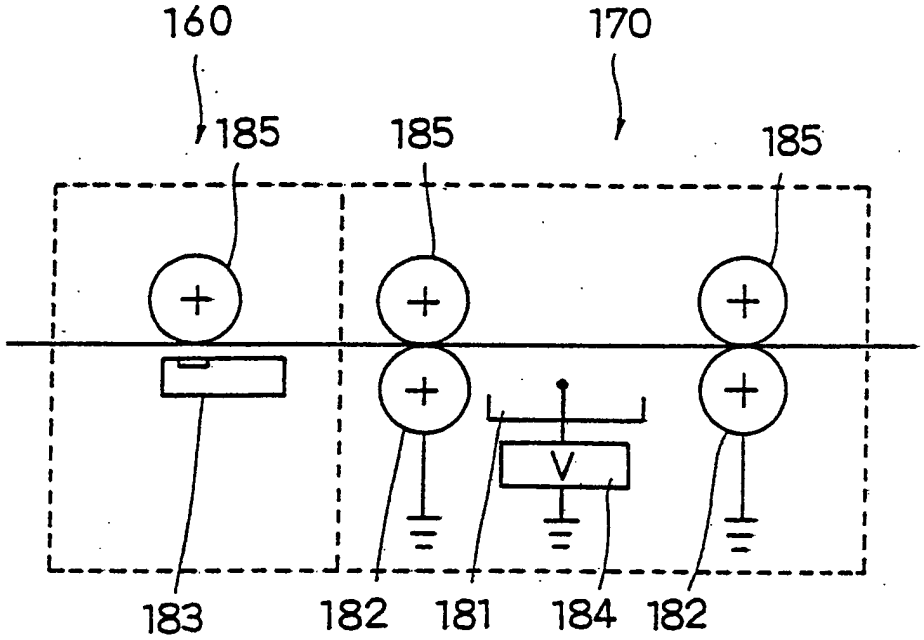
FIG. 23